Notice of References Cited

Application/Control N.			
09/944,515	Reexamination DUNCAN ET A	Reexamination DUNCAN ET AL.	
Examiner	Art Unit		
Donna K. Mason	2111	Page 1 of 1	

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-5,956,516	09-1999	Pawlowski, Stephen S.	710/260
	В	US-6,065,088	05-2000	Bronson et al.	710/263
1``	С	US-5,892,956	04-1999	Qureshi et al.	710/260
	D	US-5,619,706	04-1997	Young, Bruce	710/268
	E	US-2001/0052043	12-2001	PAWLOWSKI et al.	710/260
	F	US-6,587,868	07-2003	Porterfield, A. Kent	709/203
¥.	G	US-5,588,125	12-1996	Bennett, Brian R.	710/306
# 4	H	US-6,253,275	06-2001	Waldron et al.	710/260
. (1	US-6,604,161	08-2003	Miller, Steven	710/260
	٦	US-2001/0027502	10-2001	Bronson et al.	710/112
	к	US-6,442,634	08-2002	Bronson et al.	710/260
	L	US-6,173,351	01-2001	Garnett et al.	710/309
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Ρ				-	
Y	ď					
	R					
	Ø				+ +	- 8
	Т			·		

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	de Gloria, Alessandro, et al., "A Self Timed Interrupt Controller: A Case Study in Asynchronous Micro-Architecture Design", Sept. 19-23, 1994, IEEE Seventh Annual International ASIC Conference and Exhibit, p. 296-299.
	٧	Halang, Wolfgang A., et al., "A High-Precision Timing and Interrupt Controller to Support Distributed Real-Time Operating Systems," Aug. 13-16, 1995, Proceedings of the 38th Midwest Symposium on Circuits and Systems, Vol. 1, p. 9-12.
	W	
	Х	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.